

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Motoo ASAI, et al.

SERIAL NO: 10/509,899

GAU: 2814

FILED: June 29, 2005

EXAMINER: JAHAN, BILKIS

FOR: SUBSTRATE FOR MOUNTING IC CHIP, MANUFACTURING METHOD OF  
SUBSTRATE FOR MOUNTING IC CHIP, DEVICE FOR OPTICAL  
COMMUNICATION, AND MANUFACTURING METHOD OF DEVICE FOR  
OPTICAL COMMUNICATION

**REQUEST FOR EXTENSION OF TIME  
UNDER 37 C.F.R. 1.136**

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

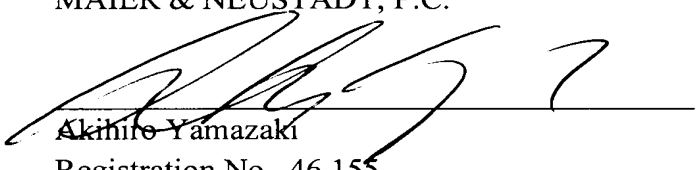
It is hereby requested that a **two** month extension of time be granted to November 23, 2008 for

- ☒ filing a response to the Official Action dated: June 23, 2008
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27.

The required fee of \$490.00 is being made by credit card payment online (if electronically filed), or is attached hereto (if paper filed), and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
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